

EAST Search History

S22	0	circuit with processor with (flash adj memory) with (power near2 input)	US-PGPUB; USPAT	OR	OFF	2006/03/20 16:46
S23	8	processor with (flash adj memory) with (power near2 input)	US-PGPUB; USPAT	OR	OFF	2006/03/20 16:46
S24	13	shipton-gary.in.	US-PGPUB; USPAT	OR	OFF	2006/03/21 10:52
S25	1	("20050078148").PN.	US-PGPUB; USPAT	OR	OFF	2006/03/21 09:52
S26	1	("4644494").PN.	US-PGPUB; USPAT; USOCR	OR	OFF	2006/03/21 10:52
S27	1	S26 and (reset with after with data)	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:14
S28	114	reset near8 after near4 writ\$4 near4 (complet\$4 or finished)	US-PGPUB; USPAT	OR	OFF	2006/03/21 10:58
S29	61747	power near2 (detect\$4 or monitor\$3)	US-PGPUB; USPAT	OR	OFF	2006/03/21 10:57
S30	3	S28 and S29	US-PGPUB; USPAT	OR	OFF	2006/03/21 10:57
S31	22	(reset near2 signal) near8 after near4 writ\$4 near4 (complet\$4 or finished)	US-PGPUB; USPAT	OR	OFF	2006/03/22 13:00
S32	2727	reset near6 after near6 (complet\$4 or finished)	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:16
S33	236	S32 and S29	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:15
S34	8	S32 same S29	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:15
S35	490	reset near6 writ\$4 near6 (complet\$4 or finished)	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:16
S36	0	S35 same S29	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:17
S37	25	S35 and S29	US-PGPUB; USPAT	OR	OFF	2006/03/21 11:17

S38 315935 integrated near2 (circuit or chip) PG-PUB; US-PAT OR OFF

S39 20 S38 with (monitor\$3 near2 power with memory) PG-PUB; US-PAT OR OFF